

Spardhaguru India Private Limited

Computer Organization

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1) Any electronic holding place where data can be	media according to the computer operating system	
stored and retrieved later whenever required is	demands.	
	a) Secondary	
a) memory	b) Virtual	
b) drive	c) Tertiary	
c) disk	d) Magnetic	
d) circuit		
	8) What is the location of the internal registers of	
2) Cache memory is the onboard storage.	CPU?	
a) True	a) Internal	
b) False	b) On-chip	
	c) External	
3) Which of the following is the fastest means of	d) Motherboard	
memory access for CPU?		
a) Registers	9) MAR stands for	
b) Cache	a) Memory address register	
c) Main memory	b) Main address register	
d) Virtual Memory	c) Main accessible register	
	d) Memory accessible register	
4) The memory implemented using the		
semiconductor chips is	10) If M denotes the number of memory location	
a) Cache	and N denotes the word size, then an expression that	
b) Main	denotes the storage capacity is	
c) Secondary	a) M*N	
d) Registers Spardhaguru	Ib) M+N Private Limited	
5) Size of the memory mainly depends on the	d) 2M-N	
size of the address bus.		
a) Main	11) What is true about memory unit?	
b) Virtual	a) A memory unit is the collection of storage units	
c) Secondary	or devices together.	
d) Cache	b) The memory unit stores the binary information	
	in the form of bits.	
6) Which of the following is independent of the	c) Both A and B	
address bus?	d) None of the above	
a) Secondary memory		
b) Main memory	12) In how many categories memory/storage is	
c) Onboard memory	classified?	
d) Cache memory	a) 2	
	b) 3	
7) storage is a system where a robotic	c) 4	
arm will connect or disconnect off-line mass storage	d) 5	
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a) To overcome the slow operating speeds of the secondary memory we make use of faster flash

b) If we use the flash drives instead of the harddisks

then the secondary storage can go above primary

c) In the memory hierarchy, as the speed of

operation increases the memory size also increases.

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20) Which of the following is true?

memory in the hierarchy.

d) Both A and C



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b) 10~50 ms

c) 10~100 ms

d) 10~500 ms



13) When when power is switched off which memory loses its data?

- a) Non-Volatile Memory
- b) Volatile Memory
- c) Both A and B
- d) None of the above

14) Auxillary memory access time is generally _ times that of the main memory

- a) 10
- b) 100
- c) 1000
- d) 10000
- 15) What is the formula for Hit Ratio?
- a) Hit/(Hit + Miss)
- b) Miss/(Hit + Miss)
- c) (Hit + Miss)/Miss
- d) (Hit + Miss)/Hit
- 16) Which of the following is correct example for Auxiliary Memory?
- a) Magnetic disks
- b) Tapes
- c) Flash memory.
- d) Both A and B

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17) The fastest data access is provided using ____

- a) Cache
- b) DRAM's
- c) SRAM's
- d) Registers

18) The next level of memory hierarchy after the L2

- a) Secondary storage
- b) Main memory
- c) Register
- d) TLB

19) Which of the following is correct refreshed rate for DRAM?

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a) 10~1000 ms

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